

Notice of References Cited	Application/Control No. 10/034,221	Applicant(s)/Patent Under Reexamination PYI, SEUNG HO	
	Examiner Matthew A. Anderson	Art Unit 1765	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-5077143	12-1991	Barracough et al.	428/690
*	B	US-6491752 B1	12-2002	Kirscht et al.	117/21
*	C	US-2002/0121676 A1	09-2002	Chu et al.	257/592
*	D	US-2002/0100917 A1	08-2002	Chu et al.	257/197
*	E	US-6426265 B1	07-2002	Chu et al.	438/312
*	F	US-5565690	10-1996	Theodore et al.	257/18
*	G	US-5561302	10-1996	Candelaria	257/24
*	H	US-5498578	03-1996	Steele et al.	437/235
*	I	US-5441901	08-1995	Candelaria	437/31
*	J	US-5360986	11-1994	Candelaria	257/183
*	K	US-2002/0179003 A1	12-2002	Iida et al.	117/19
	L	US			
	M	US			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
*	N	JP-2001237247A	08-2001	Japan	Iida et al.	H01L021/322
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Wolf et al., Silicon Processing for the VLSI Era, Volume 1: Process Technology, Lattice Press, Sunset Beach, CA, USA, pp. 1-35, 59-61, 124-159, 1986.
	V	
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.

Matthew A. Anderson *3/9/04*